

coreboot for **embedded linux developers**

Overview

- modern x86 architecture
- firmware design principles by examples
- boot flow from power on to system take off
- Coreboot walk through
- firmware build process based on Coreboot
- coreboot developer workflow
- remote testing environment
- SPI flash theory of operation
- flashing and debugging tools
- writing payloads hands-on workshop
- FSP – from theory to integration
- firmware security basics in Coreboot ecosystem
- MinnowBoard – hands-on workshop using previously gained knowledge

Audience

People familiar with Embedded Linux development and BSP building. People porting and maintaining Coreboot supported mainboards.

Materials

All training materials presentations and source code will be available for customer internal usage.



Duration

5 days

40 hours (8h/day)

70% lectures

30% hands-on workshop

Language

English.